

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

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APPLICATION NO. 09/707269	CONT/PRIOR D	CLASS 438	SUBCLASS 758	ART UNIT 2825	EXAMINER Everhart
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APPLICANTS

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Эпид

Method of modifying a wafer suited for semiconductor fabrication

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			Amount Due	Date Paid
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